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CONFIRMATION NO. 8181

SERIAL NUMBER 10/717,359	FILING DATE 11/18/2003 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. X-1376 US
APPLICANTS Sudip K. Nag, San Jose, CA;				
** CONTINUING DATA *****				
** FOREIGN APPLICATIONS *****				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/18/2004				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature Initials	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 5
ADDRESS 24309 XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR SAN JOSE , CA 95124				
TITLE Method and apparatus for timing characterization of integrated circuit designs				
FILING FEE RECEIVED 942	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	